

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Takamitsu Yamada

Application No.: 10/579,766

Confirmation No.: 3580

Filed: May 18, 2006

Art Unit: 2825

For: ASSERTION GENERATING SYSTEM,
PROGRAM THEREOF, CIRCUIT VERIFYING
SYSTEM, AND ASSERTION GENERATING
METHOD

Examiner: V. Siek

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Madam:

INTRODUCTORY COMMENTS

In response to the Office Action dated December 17, 2008, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.